

REMARKS

Entry of this Amendment in accordance with the provisions of 37 CFR §1.114, and reconsideration of this application, as amended, is respectfully requested.

This Amendment is in response to the final Office Action dated June 13, 2007. By the present Amendment, claims 30, 36 and 40 to 44 have each been rewritten into independent form to define further features of the present invention. The respective parents claims 6, 38 and 39 have been canceled at the present time, without prejudice to the applicants' right to proceed with the subject matter of these parent claims at a later date.

Reconsideration and allowance of the new independent claims 30, 36 and 40-44, together with their respective dependent claims 31 and 45, is respectfully requested. In each case, these claims define one or more of the following features:

- 1) The drain of the thin film transistor of the display data holding circuit is connected to a corresponding signal electrode;
- 2) The capacitor of the display data holding circuit is at least partially formed by a portion of the drain of the thin film resistor; and
- 3) One electrode of the capacitor is formed of a same material as a material of the drain of the thin film transistor.

It is respectfully submitted that these features, together with the features of the parent claims 6, 38 and 39, form combinations which are not taught or suggested by the cited prior art, for the reasons set forth below.

More specifically, in the second to the last paragraph at the bottom of page 5, the Office Action states:

“Hamada does not expressly disclose the display data holding circuit having one of a coplanar and an inverse stagger structure.”


The Office Action goes on the state, however, that the secondary reference to Matsuda teaches thin form transistor circuits of either coplanar or inverse stagger

structure. By virtue of the present amendments, overall combinations are defined including specific features of the thin film transistor and the capacitor of the display data holding circuit together with the claim limitations of the parent claims concerning the coplanar or inverse stagger structure. It is respectfully submitted that since the ground of rejection relies on Matsuda for the teachings of thin film transistors of a coplanar or inverse stagger structure, and since Matsuda fails to teach or suggest the above listed specific features regarding the thin film transistor and the capacitor of the display data holding circuit having such a coplanar or inverse stagger structure, the prior art fails to teach or suggest the overall claimed combinations defined by these independent claims 30, 36 and 40-44. Therefore, reconsideration and allowance of the amended claims is earnestly solicited.

To the extent necessary, applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case: 503.35282CX2), and please credit any excess fees to such deposit account.

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP

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